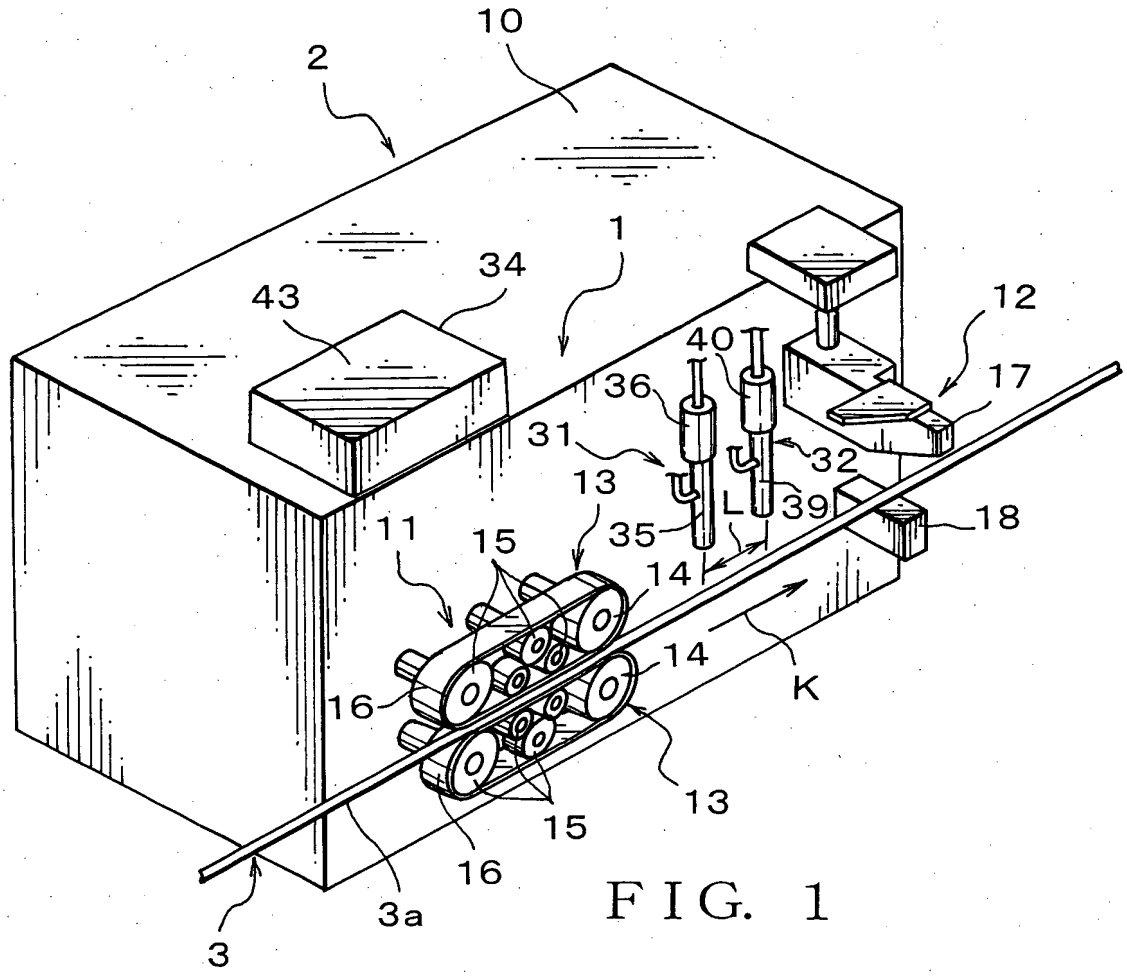
10
2
1
34
43
12
40
17
36
31
32
13
L
39
18
11
15
35
14
14
K
16
13
15
16
3a
3

FIG. 1

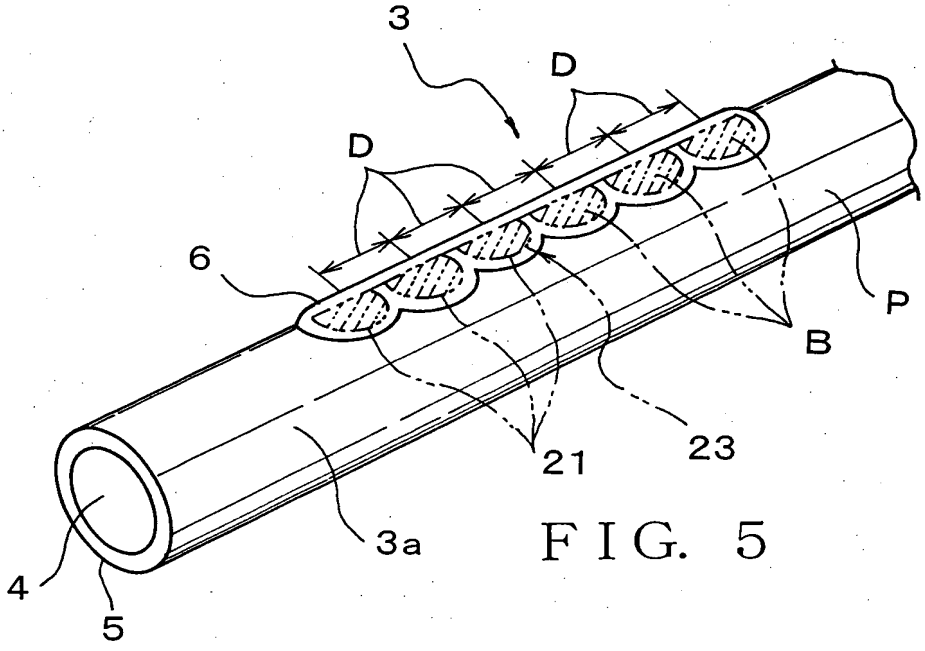
3
D
D
6
P
B
21
23
3a
4
5

FIG. 5

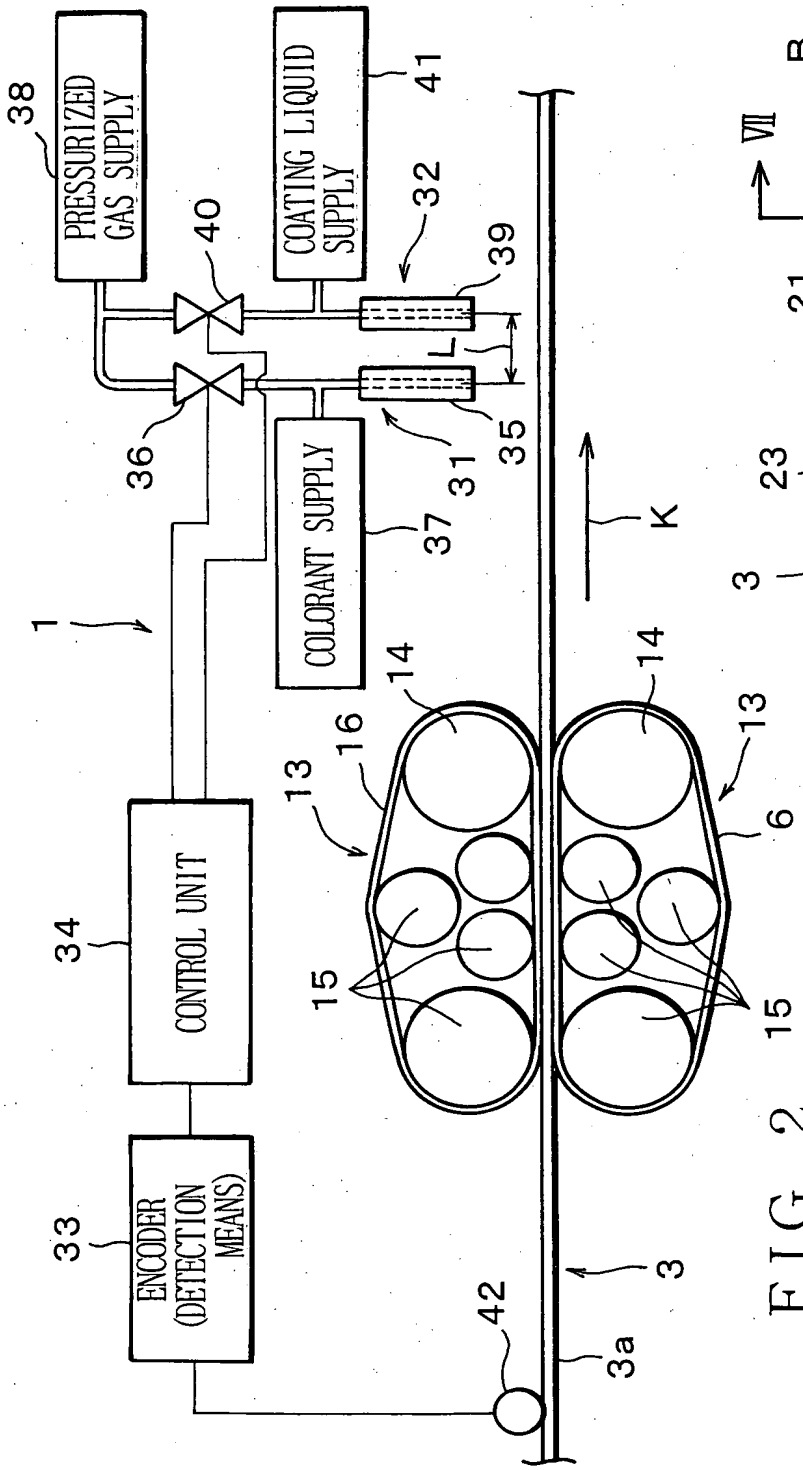
PRESSURIZED GAS SUPPLY
38
COATING LIQUID SUPPLY
41
40
32
39
L
36
31
35
COLORANT SUPPLY
37
K
1
14
14
16
13
13
6
CONTROL UNIT
34
15
15
ENCODER (DETECTION MEANS)
33
42
3
3a

FIG. 2

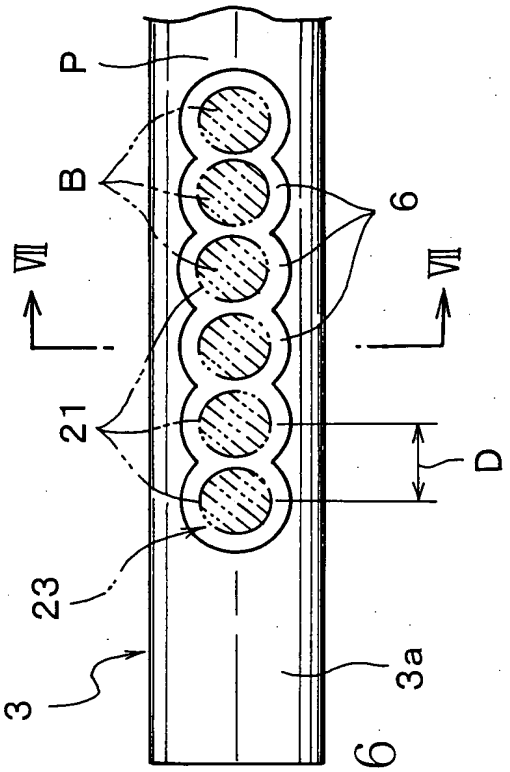
P
B
VII
VII
6
21
D
23
3
3a

FIG. 6

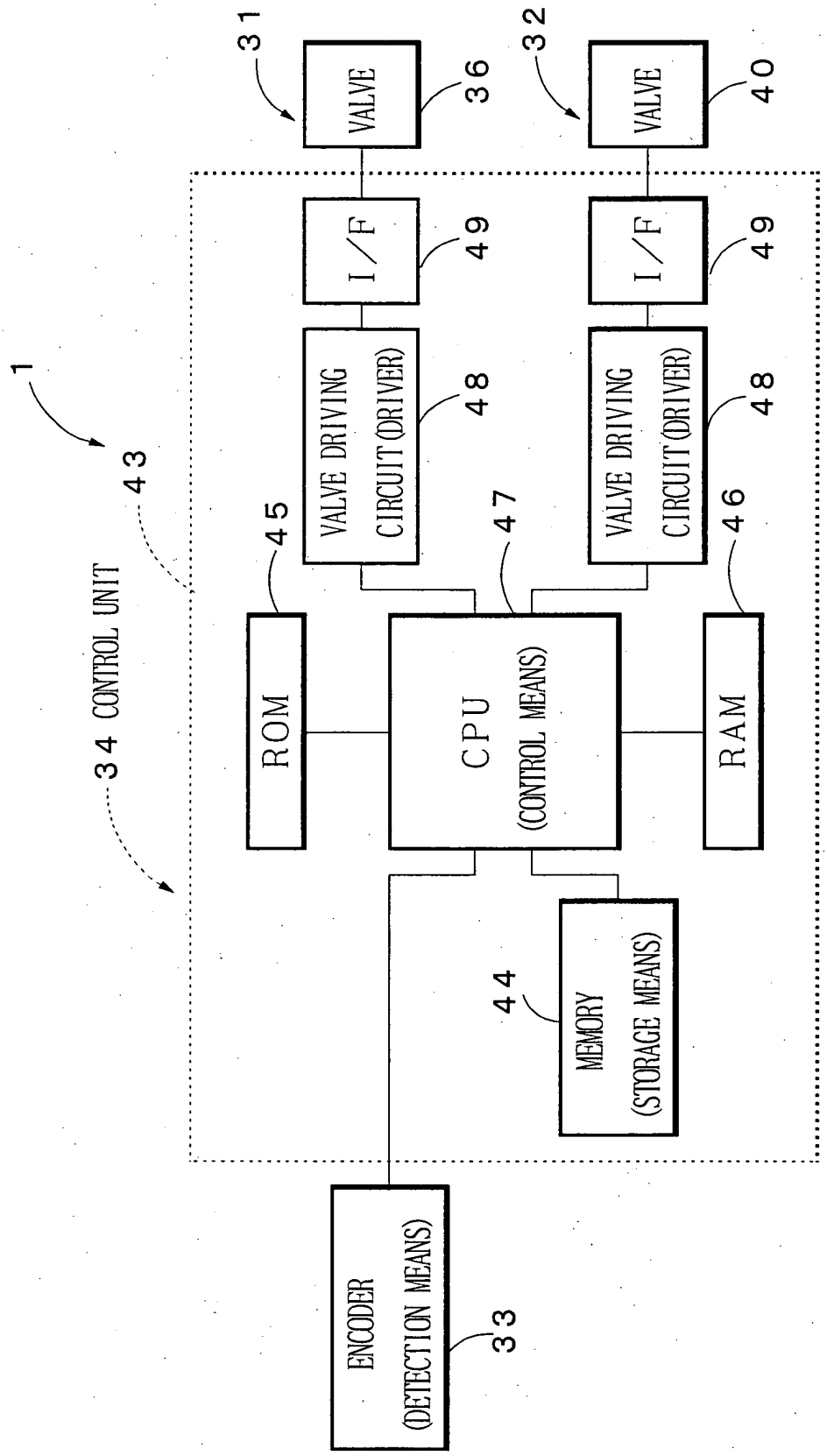
1
34 CONTROL UNIT
43
31
VALVE
36
32
VALVE
40
I/F
49
I/F
49
VALVE DRIVING CIRCUIT (DRIVER)
48
VALVE DRIVING CIRCUIT (DRIVER)
48
45
47
46
ROM
CPU (CONTROL MEANS)
RAM
44
MEMORY (STORAGE MEANS)
ENCODER (DETECTION MEANS)
33

FIG. 3

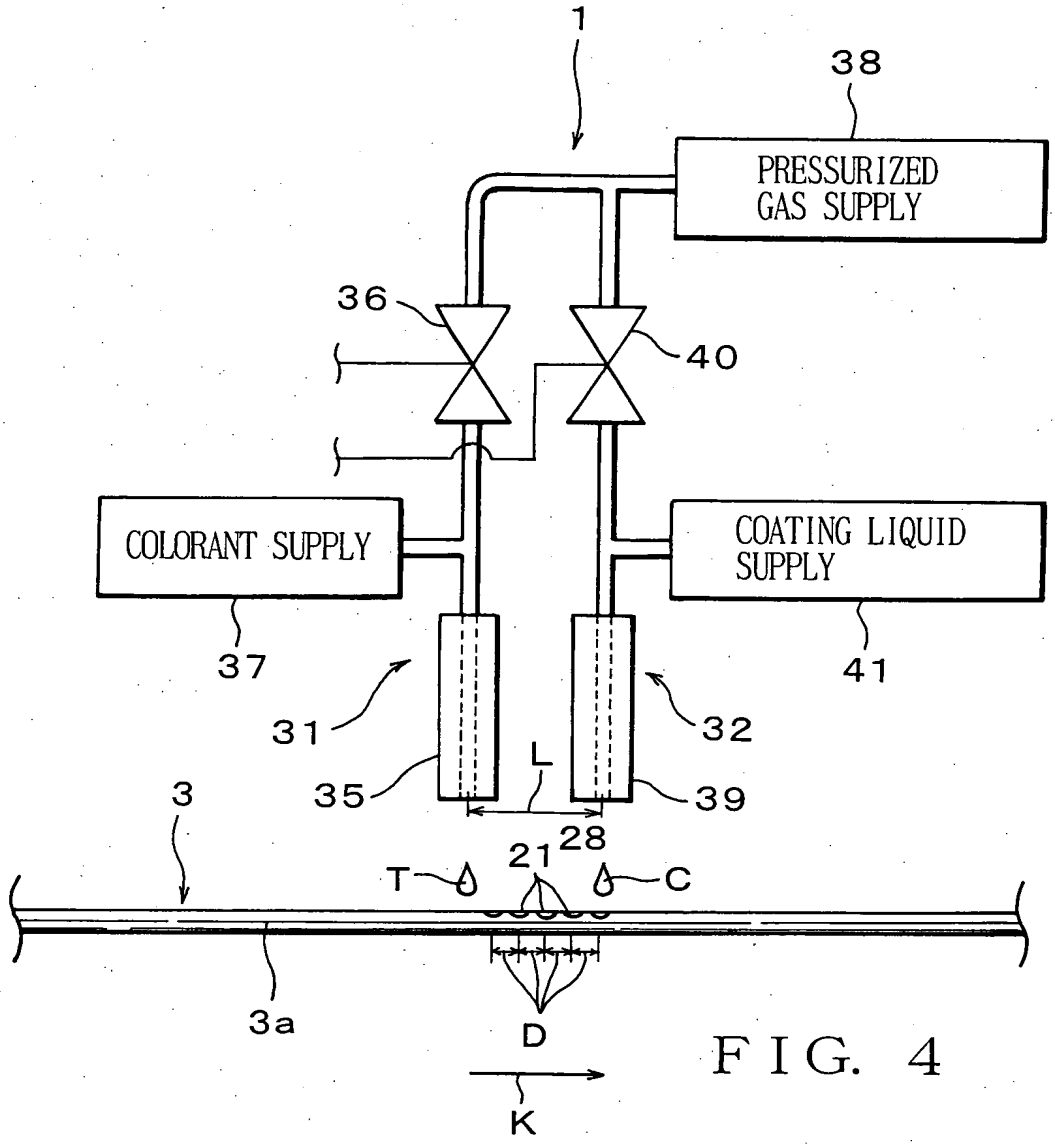
1
38
PRESSURIZED GAS SUPPLY
36
40
COLORANT SUPPLY
COATING LIQUID SUPPLY
37
41
31
32
35
L
39
3
28
21
T
C
3a
D
K

FIG. 4

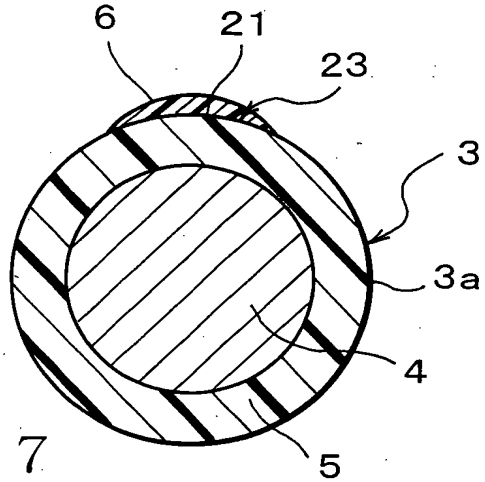
6
21
23
3
3a
4
5

FIG. 7

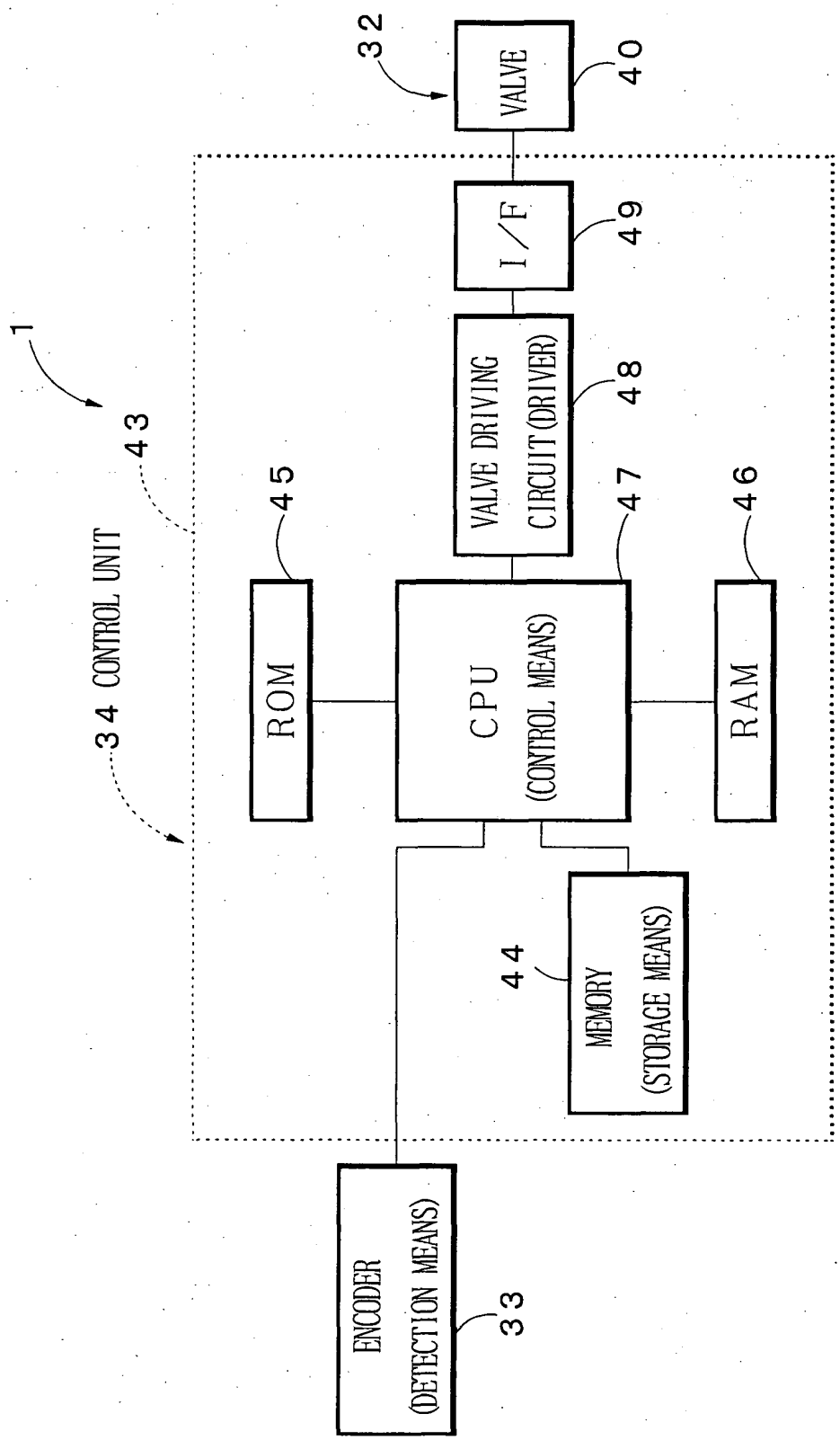
1
34 CONTROL UNIT
43
32
VALVE
40
I/F
49
VALVE DRIVING CIRCUIT (DRIVER)
48
45
47
46
ROM
CPU
(CONTROL MEANS)
RAM
44
MEMORY
(STORAGE MEANS)
ENCODER
(DETECTION MEANS)
33

FIG. 8